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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> ( Not for submission under 37 CFR 1.99)	Application Number	09989762
	Filing Date	2001-11-19
	First Named Inventor	Kenneth Y. Ogami
	Art Unit	2123
	Examiner Name	Luke R. Osborne
	Attorney Docket Number	CDO1175M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T <sup>5</sup>
	1	9532478	WO	A1	1995-11-30	Klien	Entire Document	<input type="checkbox"/>
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
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1	Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/94; 10 pages	<input type="checkbox"/>
2	Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
3	USPTO Application Number 09/924,734 (CD00169): "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; Snyder et al., filed on 08/07/01; 28 pages	<input type="checkbox"/>
4	USPTO Application Number 09/909,045 (CD00170): "Digital Configurable Macro Architecture," Warren Snyder, filed on 07/18/2001; 37 pages	<input type="checkbox"/>
5	USPTO Application Number 09/909,109 (CD00172): "Configuring Digital Functions in a Digital Configurable Macro Architecture," Warren Snyder, filed on 07/18/2001; 38 pages	<input type="checkbox"/>
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8	USPTO Application Number 09/969,311 (CD00175): "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks," Bert Sullam, filed on 10/01/01; 57 pages	<input type="checkbox"/>
9	USPTO Application Number 09/875,599 (CD00180): "Method and Apparatus for Programming a Flash Memory," Warren Snyder, filed on 06/05/2001; 23 pages	<input type="checkbox"/>
10	USPTO Application Number 09/975,115 (CD00182): "In-System Chip Emulator Architecture," Snyder et al., filed on 10/10/2001; 38 pages	<input type="checkbox"/>
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Examiner Signature	/Luke Osborne/	Date Considered	10/14/2009
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